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<p>INFORMATION DISCLOSURE STATEMENT</p> <p>PTO-1449</p>	<p>Atty. Docket No. ABE-034</p>	<p>Serial No. 10/550761 Not assigned</p>
	<p>Applicant: Eiji KAMIYAMA et al.</p>	
	<p>Filing Date: September 27, 2005</p>	<p>Group: 1763 Not assigned</p>

U.S. PATENT DOCUMENTS

Examiner's Initial		Document No.	Date	Name	Class	Sub Class	Filing Date If appropriate
/RC/	UA	5,882,987 A	03/16/99	Srikrishnan	438	458	
/RC/	UB	6,140,210 A	10/31/00	Aga et al.	438	458	
/RC/	UC	6,372,609 B1	04/16/02	Aga et al.	438	459	
/RC/	UD	5,374,564 A	12/20/94	Bruehl	437	24	
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/RC/	FA	11-121377 A	04/30/99	JP	Abstract
/RC/	FB	11-102848 A	04/13/99	JP	Abstract
/RC/	FC	2000-124092 A	04/28/00	JP	Abstract
/RC/	FD	2003-17723 A	01/17/03	JP	Abstract & Translation
/RC/	FE	2004-80035 A	03/11/04	JP	Abstract & Translation
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/RC/	DA	Auberton-Hervé, A.J. et al., "SMART-CUT®: The Basic Fabrication Process for UNIBOND® SOI Wafers," <i>IEICE TRANS ELECTRON</i> , Vol. E80 C, No. 3, 1997, pp. 358-363.
/RC/	DB	Tong, Q.Y. et al., "SemiConductor Wafer Bonding: Science and Technology"; November, 1998; printout of website http://www3.interscience.wiley.com:8100/WileyCDA/WileyTitle/productCd-0471574813_descCd-description.html?print=true .
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<p>Examiner: /Roberts Culbert/</p>	<p>Date Considered: 08/05/2007</p>
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KTK/jbf

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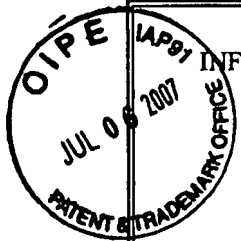
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/RC/	DA	Auberton-Hervé, A.J. et al., "SMART-CUT®: The Basic Fabrication Process for UNIBOND® SOI Wafers," <i>IEICE TRANS ELECTRON</i> , Vol. E80 C, No. 3, 1997, pp. 358-363.
/RC/	DB	Tong, Q.Y. et al., "SemiConductor Wafer Bonding: Science and Technology"; November, 1998; printout of website http://www3.interscience.wiley.com:8100/WileyCDA/WileyTitle/productCd-0471574813_descCd-description.html?print=true .
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		Applicant: Eiji KAMIYAMA et al.	
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/RC/	FC	1 085 562 A2	03/21/01	EP	
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